

Electronic Acknowledgement Receipt

EFS ID:	1157198
Application Number:	10749464
Confirmation Number:	3974
Title of Invention:	Logical to physical address mapping of chip selects
First Named Inventor:	Derek A. Thompson
Customer Number:	6980
Filer:	Jeffrey L. Waters
Filer Authorized By:	
Attorney Docket Number:	INTEL4
Receipt Date:	16-AUG-2006
Filing Date:	31-DEC-2003
Time Stamp:	15:14:57
Application Type:	Utility
International Application Number:	

Payment information:

Submitted with Payment	no
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File Listing:

Document Number	Document Description	File Name	File Size(Bytes)	Multi Part	Pages
1		intel4response.pdf	103120	yes	9

	Multipart Description		
	Doc Desc	Start	End
	Amendment - After Non-Final Rejection	1	1
	Specification	2	3
	Claims	4	6
	Applicant Arguments/Remarks Made in an Amendment	7	9

Warnings:

Information:

Total Files Size (in bytes):	103120
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New Applications Under 35 U.S.C. 111

If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.

National Stage of an International Application under 35 U.S.C. 371

If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.